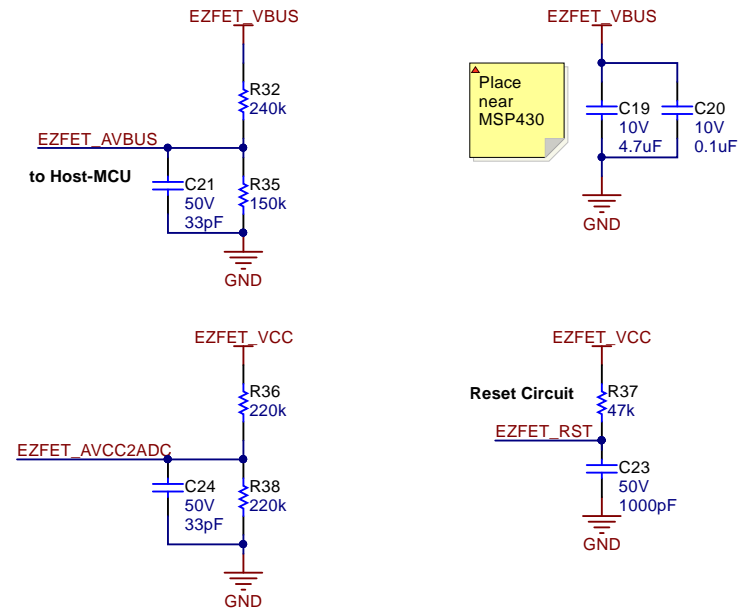
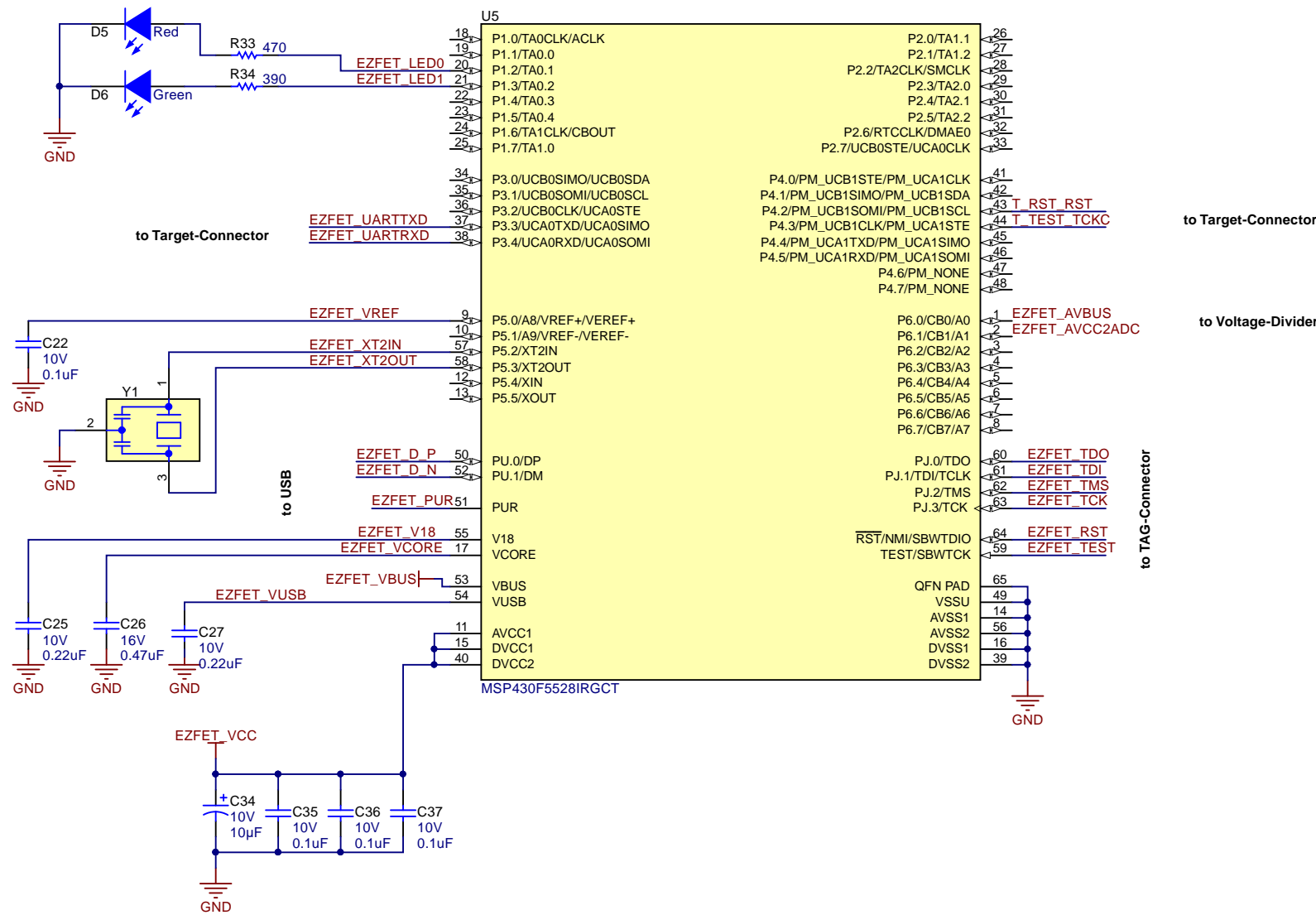


1	2	3	4	5	6										
A	<div>Revision History</div> <table><tr><td>Rev</td><td>ECN #</td><td>Approved Date</td><td>Approved by</td><td>Notes</td></tr><tr><td>N/A</td><td>N/A</td><td>N/A</td><td>N/A</td><td>N/A</td></tr></table>					Rev	ECN #	Approved Date	Approved by	Notes	N/A	N/A	N/A	N/A	N/A
						Rev	ECN #	Approved Date	Approved by	Notes					
						N/A	N/A	N/A	N/A	N/A					
						B	C	D							
1	2	3	4	5	6										

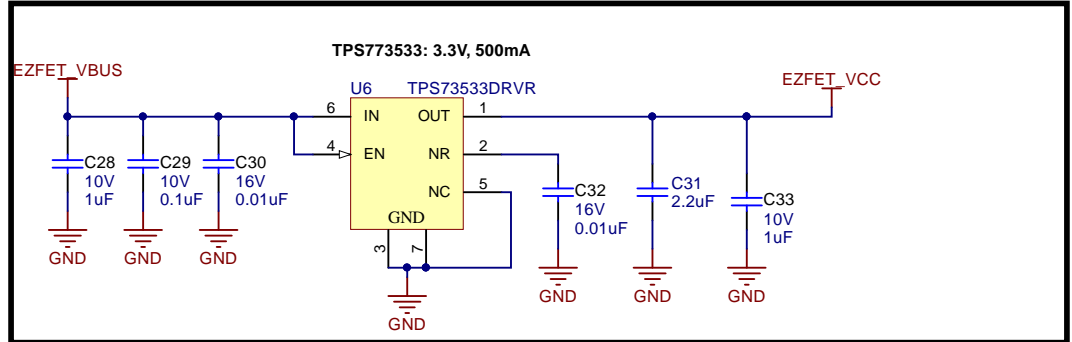
Place Block Diagram here (if appropriate) or delete this text box.
If using a block diagram from another tool, save the picture as a .bmp file.
Then, use menu Place|Drawing Tools|Graphic to insert the .png/.svg/.bmp file on the schematic.

Orderable: DRV8245H-Q1LEV		Designed for: Public Release		Mod. Date: 8/28/2020	
TID #: N/A		Project Title: DRV8245S/H-Q1EVM HTSSOP			
Number: MD048		Rev: E2		Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 002		Sheet: 1 of 3	
Drawn By:		File: CoverSheet.SchDoc		Size: B	
Engineer: Michael Erdahl		Contact: http://www.ti.com/support		http://www.ti.com	
© Texas Instruments 2020					

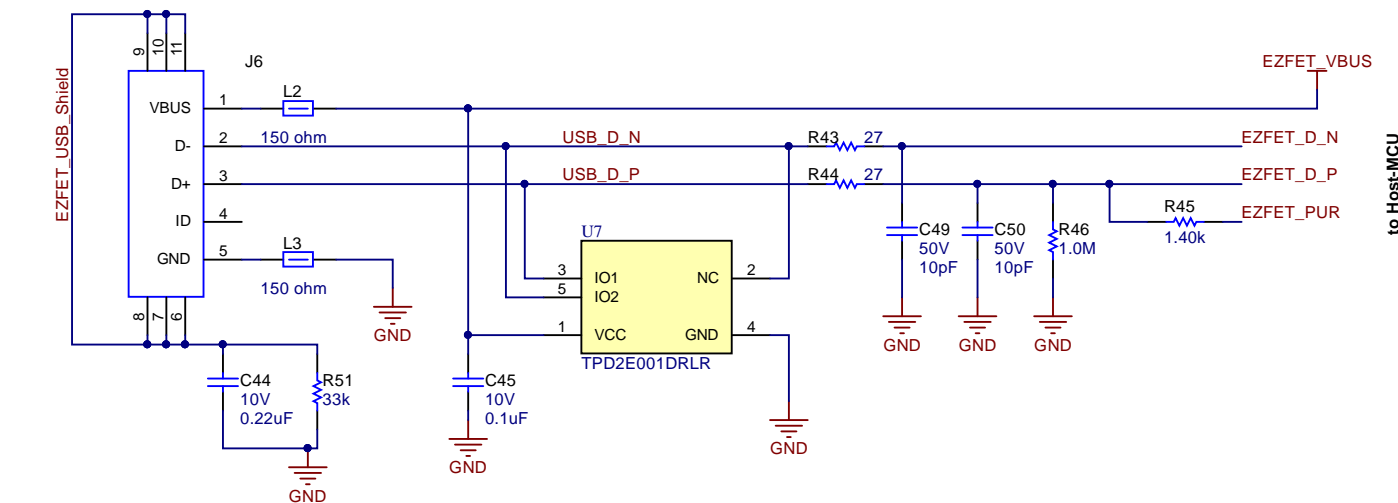
Host MCU for Emulation



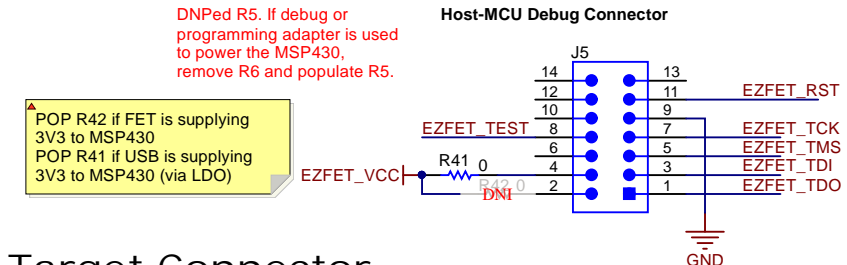
3.3V Power (EZFET_VCC)



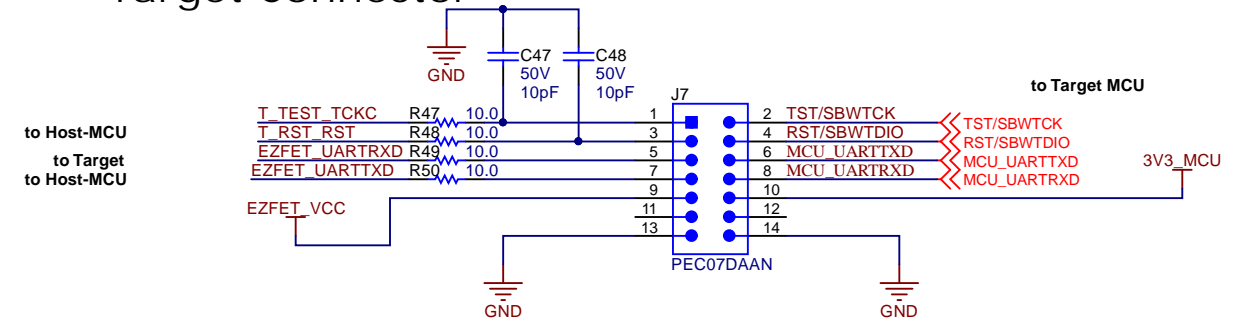
USB-Interface



TAG-Connector (Host Debug)



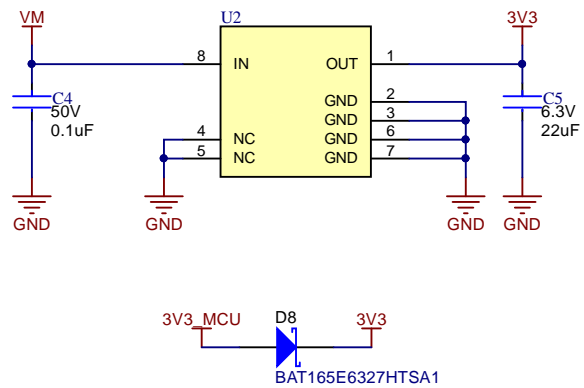
Target Connector



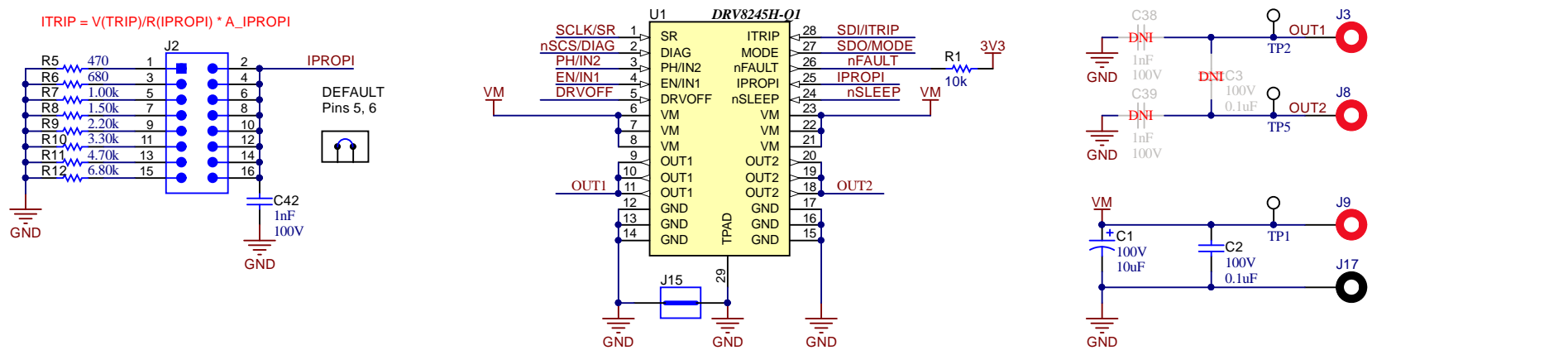
Orderable: DRV8245H-Q1LEVM	Designed for: Public Release	Mod. Date: 1/15/2021	
TID #: N/A	Project Title: DRV8245S/H-Q1EVM HTSSOP		
Number: MD048	Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 002	Sheet: 2 of 3	
Drawn By:	File: ezFET.SchDoc	Size: B	
Engineer: Michael Erdahl			http://www.ti.com © Texas Instruments 2020

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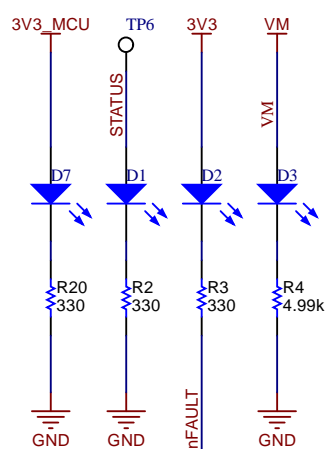
3.3V LDO



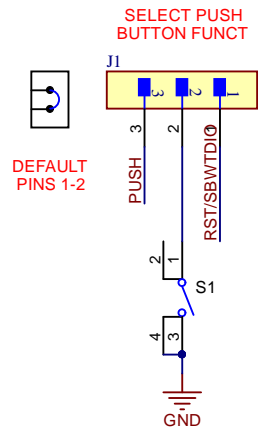
DRV8245S/H-Q1 HTSSOP



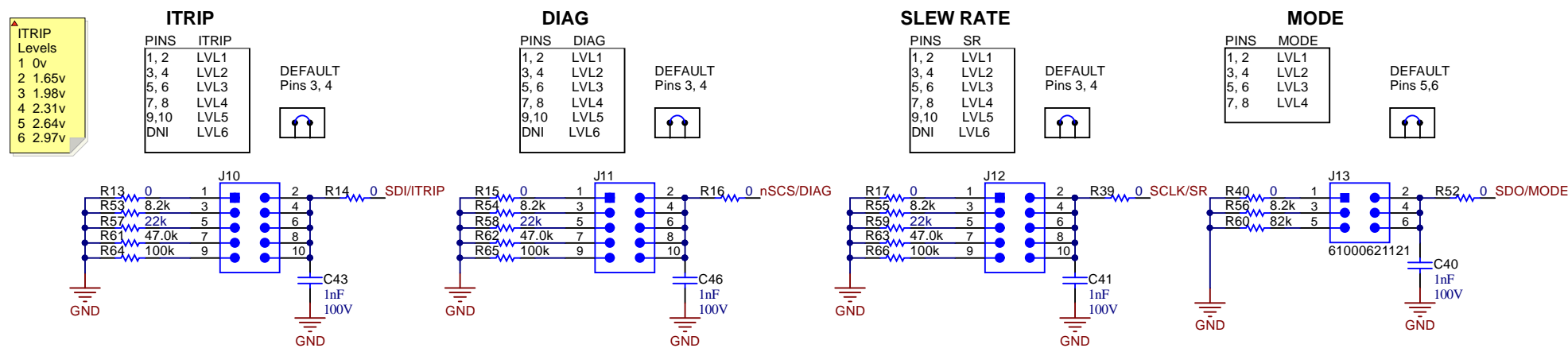
LEDS



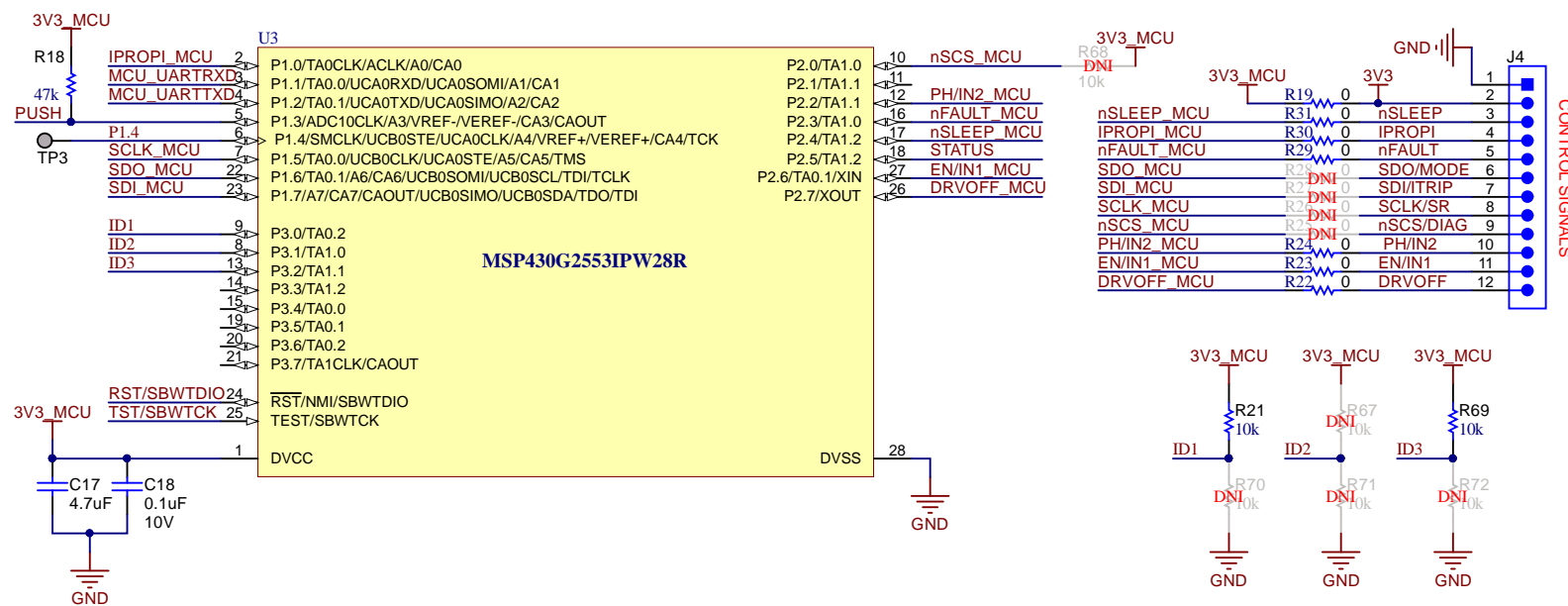
BUTTON



ANALOG CONTROL SIGNALS (H-variant only)



MSP430



H1
SJ-5303 (CLEAR)

H2
SJ-5303 (CLEAR)

H3
SJ-5303 (CLEAR)

H4
SJ-5303 (CLEAR)

DNI
FID1

DNI
FID2

DNI
FID3

PCB Number: MD048
PCB Rev: E2

PCB
LOGO
Texas Instruments

CE Mark

PCB
LOGO
FCC disclaimer

PCB
LOGO
WEEE logo

CAUTION HOT SURFACE

PCB
LOGO
CAUTION. READ USER GUIDE BEFORE USE

LBL1

PCB Label

THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1

Label Assembly Note

This Assembly Note is for PCB labels only

Variant/Label Table	
Variant	Label Text
001	DRV8245S-Q1LEVM
002	DRV8245H-Q1LEVM

ZZ2

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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